



CofC

Attorney Docket No. 5649-886

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Jung et al.

U.S. Patent No.: 7,246,250 B2

Application No.: 09/941,091

Issued: June 17, 2007

Filed: August 28, 2001

For: MEMORY DEVICE CONTROLS DELAY TIME OF DATA INPUT BUFFER IN  
RESPONSE TO DELAY CONTROL INFORMATION BASED ON A POSITION OF A  
MEMORY DEVICE RECEIVED FROM MEMORY CONTROLLER

August 9, 2007

Commissioner for Patents  
Attn: Certificate of Correction Branch  
P.O. Box 1450  
Alexandria, VA 22313-1450

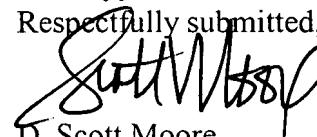
**REQUEST FOR ENTRY OF CERTIFICATE OF CORRECTION UNDER  
35 U.S.C §254 AND 37 C.F.R. §1.322**

Sir:

The Assignee of record for the above-referenced patent hereby requests, pursuant to 35 U.S.C §254 and 37 C.F.R. §1.322, that a Certificate of Correction be issued. This request is made in order to correct the mistakes incurred through the fault of the U.S. Patent and Trademark Office. No fee is believed due. However, the Commissioner is authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

The mistakes appearing in the patent are set forth with corrections on the Certificate of Correction enclosed herewith, with an additional copy thereof, and a return post card.

Respectfully submitted,

  
D. Scott Moore  
Registration No. 42,011

**Certificate**

Myers Bigel Sibley & Sajovec, P.A.  
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Customer No. 20792

AUG 15 2007

**of Correction**

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Attn: Certificate of Correction Branch, P.O. Box 1450, Alexandria, VA 22313-1450, on August 9, 2007.

  
Jessica M. French

AUG 16 2007

UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION

PATENT NO. : 7,246,250 B2

DATED : June 17, 2007

INVENTOR(S) : Jung et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In The Claims:

Column 9, Claim 3, Line 52: Please correct "memory device"  
To read -- memory devices --

Column 10, Claim 8, Line 39: Please correct "according to the"  
To read -- according to a --

Column 10, Claim 8, Line 43: Please correct "delay information"  
To read -- control information --

Column 10, Claim 8, Line 44: Please correct "delay control"  
To read -- predetermined control --

Column 10, Claim 8, Line 46: Please correct "respond to the"  
To read -- respond to an --

Column 11, Claim 12, Line 15: Please correct "receiving delay control"  
To read -- receiving the control --

Column 11, Claim 12, Line 17: Please correct "received delay control"  
To read -- received control --

Column 12, Claim 20, Line 12: Please correct "controlling the delay"  
To read -- controlling a delay --

Column 12, Claim 24, Line 51: Please correct "that is based on position"  
To read -- that is based on a position --

AUG 16 2007

Column 14, Claim 34, Line 37: Please correct  
"operating an integrated circuit memory"  
To read -- operating a memory --

Column 14, Claim 34, Line 41: Please correct "information from that is"  
To read -- information that is --

MAILING ADDRESS OF SENDER:  
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→  
This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you are required to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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